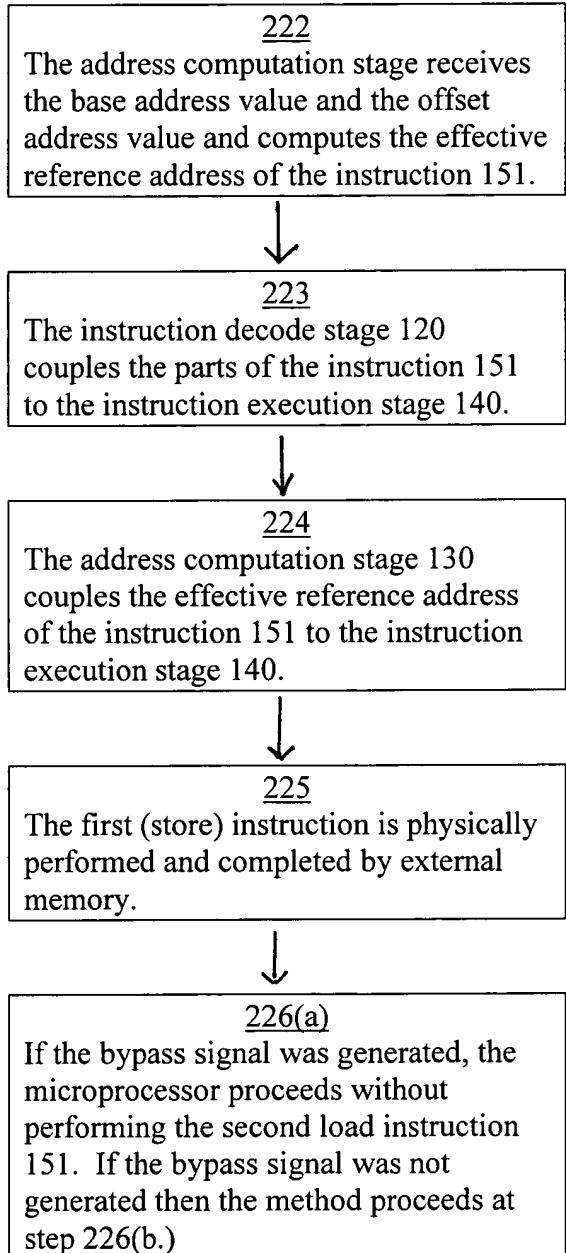


FROM FIG. 2A



META-012
Fig. 2b

226(b)

If the bypass signal was not generated (or if an inverse bypass signal was generated), the second load instruction 151 is physically performed and completed by external memory.